

Notice of References Cited	Application/Control No. 10/705,413		Applicant(s)/Patent Under Reexamination MORISHITA ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,926,317	05-1990	Wallach et al.	711/3
*	B	US-5,983,329	11-1999	Thaler et al.	711/163
*	C	US-2002/0087824	07-2002	Hum et al.	711/202
*	D	US-5,347,643	09-1994	Kondo et al.	711/211
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Patterson, David A., John L. Hennessy. "Computer Organization and Design: The Hardware/Software Interface." 2 nd ed. Morgan Kaufmann. San Francisco, CA: 1997			
	V	Handy, Jim. "The Cache Memory Book." 2 nd ed. Academic Press LTD. London, UK: 1998			
	W	Marty, Emmanuel. "Review of Operating Systems." 23 Apr. 2000. < http://www2.tunes.org/Review/OSes.html >.			
	X	"Cache." Wikipedia: The Free Encyclopedia. 29 Jan 2006. < http://en.wikipedia.org/wiki/Cache >.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chioiu, Derek et al. "Application-Specific Memory Mangement in Embedded Systems Using Software-Controlled Caches." Proceedings of the 37 th Design Automation Conference Architecture, June, 2000. Cambridge, Department of EECS, MIT, 2000.
	V	Suh, G Edward et al. "Dynamic Cache Partitioning for Simultaneous Multithreading Systems." 13 th IASTED International Conference on Parallel and Distributed Computing Systems, 2001. Cambridge, MIT Laboratory for Computer Science, 2000.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.